

The fundamental idea is to take all of the event methods E(args) in a parallel discrete event simulation and "factor" each of them into three parts: E+(args), E-(), and E*().

E+(**args**) is executed in place of E(args) in the simulation and is instrumented to save all information destroyed by the forward execution of **E**(**args**) so as to preserve the option after **E**(**args**) completes of restoring the initial state of an object before it executed.

 \mathbf{E}^{-} () uses the information stored by \mathbf{E}^{+} () and also the object's state information to exactly reconstruct the state before \mathbf{E}^{+} () executed. It in effect reverses all of the side effect of \mathbf{E}^{+} () and exactly accomplishes rollback of the event.

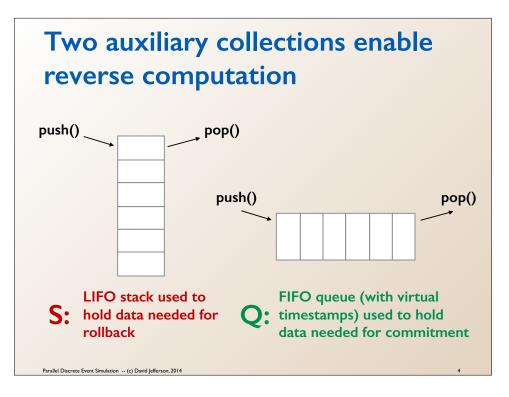
E^{*}() is executed at the time event **E**(**args**) is committed, and deals with actions specified in **E**⁺(**args**) that really cannot be rolled back, such as output, or the freeing of dynamically allocated storage.

The two equations boxed in red are properties that the three methods must satisfy.

The first one says the $\mathbf{E}^{-}(\mathbf{)}$ really does reverse all of the side effects of $\mathbf{E}^{+}(\mathbf{args})$, and does nothing else, so that executing $\mathbf{E}^{+}(\mathbf{args})$ followed by $\mathbf{E}^{-}(\mathbf{)}$ is a no-op.

The second one says that executing \mathbf{E}^+ (**args**) followed by \mathbf{E}^* () is equivalent to executing the original method E(args).

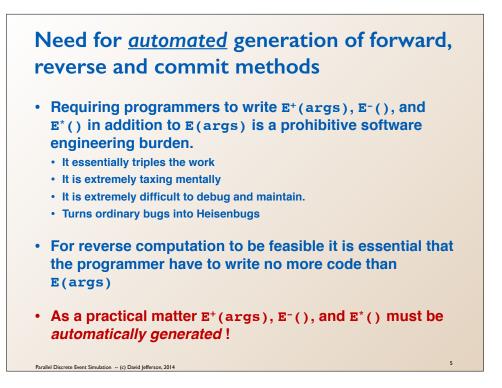
Since every time **E**⁺(**args**) is executed it will either be rolled back or committed, then either **E**⁻() or **E**^{*}() will be executed after it and the net effect will either be a no-op (in the case of a rollback) or it will be as if **E**(**args**) executed (in the case of commitment).



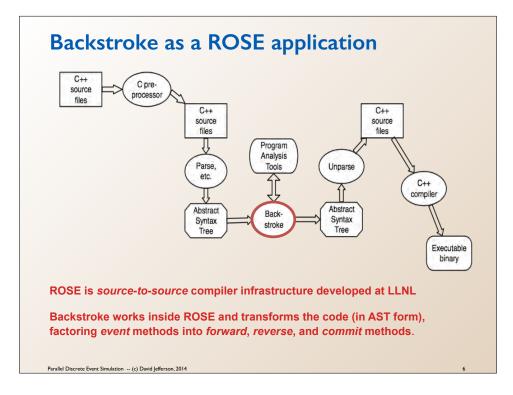
In factoring an event method we generally use two auxiliary data structures. One, which we call S in these slides, is a LIFO stack that is used to hold data needed for rollback. The LIFO structure is natural, because the data that we need for executing \mathbf{E}^- () is need in the reverse order of the order in which it was put in during forward execution of \mathbf{E}^+ ().

The other we are calling Q, and it is a FIFO queue of data to be saved for $\mathbf{E}^{*}()$ at commitment time. Since the things done at commitment time (e.g. output) have to be done in the same order as specified during execution of $\mathbf{E}^{+}()$, it is natural for a FIFO queue to be employed.

The "**push**", "**pop**" and later "**top**" and "**front**" terminology are from the C++ STL.

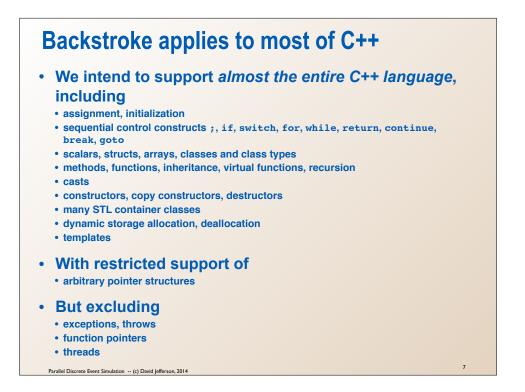


LLNL has a project called Backstroke that is intended to automatically generate reverse code for almost any code written in C++.

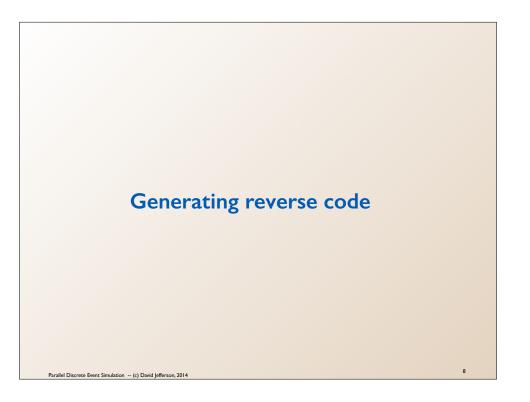


This is a diagram of the workflow in ROSE, a general source-to-source program transformation system and compiler. Backstroke is the component in **red** that uses ROSE's powerful program analysis tools and adds forward, reverse and commit routines for all event methods in a ROSS-compatible simulation.

It is not absolutely essential to construct a reverse code generator this way. You could go directly from the revised Abstract Syntax Tree to executable binary. (The LORAIN project at RPI, based on LLVM, is structured that way.) But by going back through source code, the programmer can see what the forward, reverse, and commit methods look like, and perhaps learn how to improve their performance.



Some parts of the C++ language are straightforward to support. Some are quite tricky. Some may benefit greatly from programmer advice. and some are so difficult to support that it will never be worth the effort to try. If generating reverse code for legacy code we may need hand work to rewrite those parts that use features of the language that are not conducive to reversibility. For new code we need to advise programmers to avoid certain language constructs that will preclude automatic generation of reverse code.

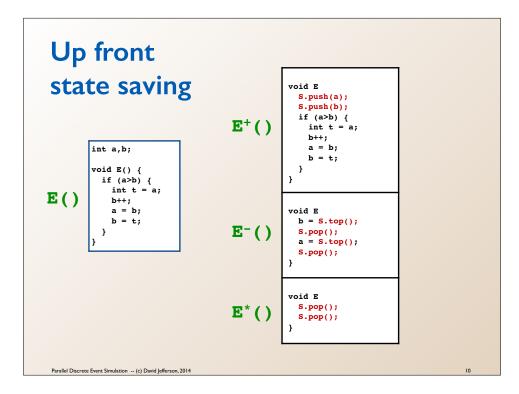


	E()	E+()	E-()
Additive integer assignment	i = i + m;	i = i + m;	i = i - m;
Floating point	$\mathbf{x} = \mathbf{x} + \mathbf{y};$	$s_{y} push(x);$ x = x + y;	<pre>x = S.top(); S.pop();</pre>
General assignment	x = f(x,y);	<pre>S-push(x); x = f⁺(x,y);</pre>	<pre>x = S.top(); S.pop(); f⁻();</pre>
Sequential composition	P;R;	P* ; R* ;	R- ; P- ;
Conditional w/ side-effect free test	if (test) { P; } else { R; }	<pre>if (test) { P*; S.push(1); } else { R*; S.push(0); }</pre>	<pre>if (S.top()) { S.pop(); P; } else { S.pop(); R; }</pre>
WHILE-loop with side-effect free test	while (test) { P; }	<pre>i = 0; while (test) { P'; i=i+1; } S.push(i);</pre>	<pre>i = S.top(); S.pop(); while (i>0) { i=i-1; P⁻; }</pre>

 ${f s}$ is the global stack onto which all saved data is pushed that is required for rolling back forward execution.

For the tests in both the conditional and the while loop, we assume there are no side effects. If there are, then the reverse code can to be easily adjusted.

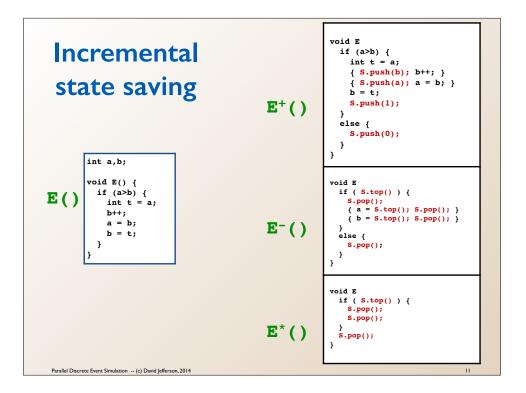
Note that integer increments / decrements and sequential composition require no data to be stored on the stack **s**.



With up front state saving we identify all of the state variables that *might* change and push their values on the stack at the beginning of the forward routine. We do not have to record which branch of the conditional was taken, and if there were a loop in the method body we would not need to record how many times the body was executed. In this case both state variables do change. But if there were more variables in the state and we could not determine statically that some of them would not change during execution of E() we would just introduce code in $E^+()$ and $E^-()$ to save and restore them all. In $E^-()$ we simply restore the values of **a** and **b** and pop the stack. We do not have to pay any attention to the algorithm used in E().

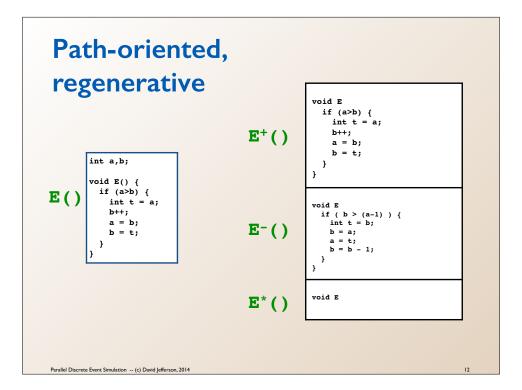
The commit method $\mathbf{E}^{\star}(\mathbf{)}$ must also pop any values off the stack that were pushed there by the forward method because the commit method is called if and only if the reverse method $\mathbf{E}^{-}(\mathbf{)}$ is *not* called.

Note also that variable **t** here is only a temp. It is not a state variable in the simulation, and hence it does not have to be restored during rollback.

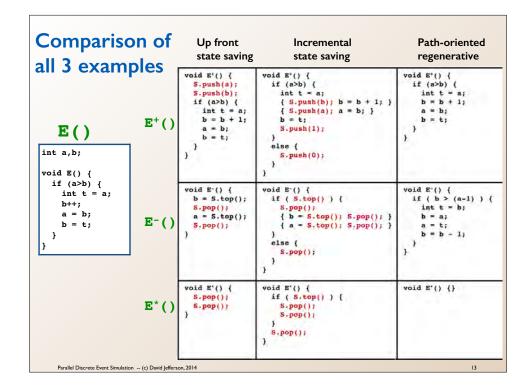


With Incremental State Saving we instrument the forward routine $\mathbf{E}^+(\mathbf{j})$ to save the value of a state variable the very first time it is overwritten or, if we cannot determine that statically, then we save the value every time it is overwritten that *might possibly be the first time*. In the forward method $\mathbf{E}^+(\mathbf{j})$ we save its value only the first time if we can because of course in the reverse method we only need to restore it to its *initial* value. In this example the variable **b** is overwritten twice, but we push its value onto the stack only the first time. Of course we also have to push a boolean indicating which branch of the conditional was taken. The reverse method, $\mathbf{E}^-(\mathbf{j})$, only restores the variable to their original values and pops the stack.

The commit method $\mathbf{E}^{\star}(\mathbf{)}$ must also pop any values off the stack that were pushed there by the forward method because the commit method is called if and only if the reverse method $\mathbf{E}^{-}(\mathbf{)}$ is *not* called.



In this case the path-oriented and regenerative style of reverse code generation manages to produce *perfectly reversible code*, with nothing pushed onto or popped from the stack. Not that the forward $\mathbf{E}^+()$ routine in this case is identical to $\mathbf{E}()$ and the commit routine $\mathbf{E}^*()$ is a no-op. We did not even have to introduce any additional variables. Perfect reversibility is not usually achievable for a whole event method, but it often is for at least some regions of the code and/or some variables. It is the ideal of reverse computation.



This slide just summarizes the last three examples. The original code for \mathbf{E} () is in a box on the left, and three different ways of factoring it into \mathbf{E}^+ (), \mathbf{E}^- (), and \mathbf{E}^+ () are recorded in the next three columns of the table.

The comparison shows that in this particular example, if the condition (**a>b**) is true, then there is more overhead with incremental state saving then there is with up front state saving, but if (**a>b**) is false then the reverse is true. This is not a general statement, however.

Also in this case the path-oriented regenerative methods generate perfect reverse code that does not need to save any data on the stack at all. In this case the code it produces is clearly superior to either of the other methods.

E()	E+()	E-()
<pre>int a, b, c; void E() { if (a > 0) { b = a + 10; a = 3; } if (c == 0) c = 5; else c = 7; }</pre>	<pre>void E+() { int path = 0; if (a > 0) { S.push(b); b = a + 10; a = 3; } else path = 2; if (c == 0) c = 5; else { path = 1; S.push(c); c = 7; } S.push(path); }</pre>	<pre>void E-() { int path = S.top(); S.pop(); if (path & 1) { c = S.top(); S.pop(); } else c = 0; if ((path & 2) == 0) { a = b - 10; b = S.top(); S.pop(); } }</pre>

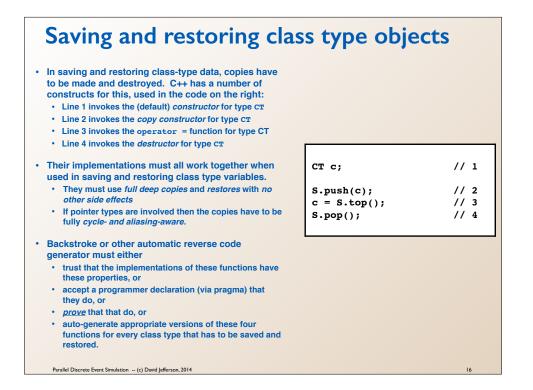
This is another example application of Backstroke's path-oriented regenerative inversion algorithm. The variables **a**, **b**, and **c** are state variables. The forward code is instrumented in **red** to keep track of the dynamic path taken, and the reverse code uses path information to restore variable values. Note that the variable **path** that is introduced in the forward and reverse methods should be viewed as a bit mask that records for each conditional which branch on the conditional was taken. In the forward routine the low order bit of **path** records is **0** if the **then**-branch of the second conditional is taken, and is a **1** if it is not. The second bit records the same thing for the first conditional. In the reverse method the corresponding conditionals are reversed in order, so the low order bit indicates which branch to take with the second conditional.

· · · · · · ·	E()	E+()	E-()
General assignment	<pre>void E() { P; x = f(x,y); R; }</pre>	<pre>void E*() { P*; S.push(x); x = f*(x,y); R*; }</pre>	<pre>void E⁻() { R⁻; x = S.top(); S.pop(); f⁻(); P⁻; }</pre>
not practic A function 	al.	ne cases by inlining, l of E into two <i>regions</i>	
 not practic A function after f(). It requires 	al. call splits the body	of E into two <i>regions</i> ore an intermediate s	: before f() and
 not practic A function after f(). It requires the function 	al. call splits the body us to be able to rest n was called, not ju:	of E into two <i>regions</i> ore an intermediate s	: before £() and
 not practice A function after f (). It requires the function In this call 	al. call splits the body us to be able to rest on was called, not just ase that is the state just	of E into two <i>regions</i> ore an intermediate s st the initial state.	: before £() and state, right where in E ⁺ ()
 not practice A function after f(). It requires the function In this ca This is a Up from 	al. call splits the body us to be able to rest on was called, not just ase that is the state just contrast between regio	of E into two regions ore an intermediate s st the initial state. after the execution of P ⁺ n-based and incrementa t be interpreted as up fro	: before £() and state, right where in E ⁺ () l inversion methods.

s is the global stack onto which all saved data is pushed that is required for rolling back forward execution.

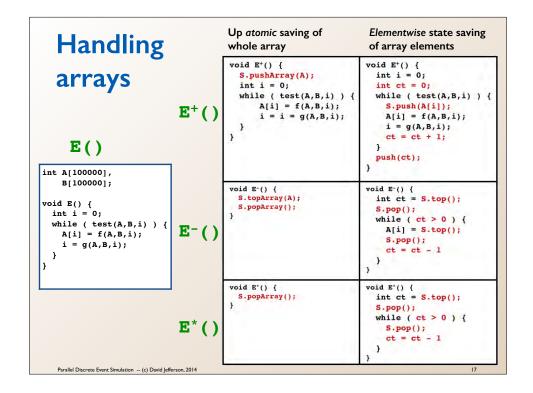
For the tests in both the conditional and the while loop, we assume there are no side effects. If there are, then the reverse code can to be easily adjusted.

Note that integer addition / subtraction and sequential composition require no data to be stored on the stack **s**.



The semantics of C++ constructors, copy constructors, destructors, and assignment operators all play a fundamental role in the way reverse computation is implemented when class-type values are involved. We cannot assume that the destructor is the reverse of the constructor, and we cannot assume that they all play well together and have the properties needed in the general case for reverse computation, namely that full, deep copies are made that are aliasing- and cycle-aware and that they have no other side effects except those required for perfect copies.

In C++ it is also useful in some patterns to create uncopyable or unassignable types, and those simply should not be used as the values of state variables in a simulation.

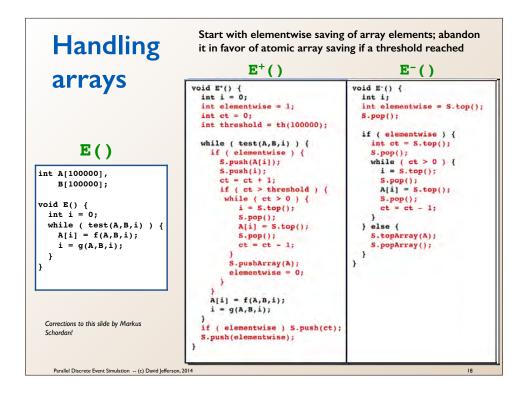


In this example the **A** and **B** arrays are state variables, but **i** is not. And the functions **test**, **f**, and **g** are side-effect free.

For the purposes of exposition on this slide we have assumed the existence of methods **S.pushArray(A)**, **topArray(A)**, and **S.popArray()** that do for array arguments the same things as **S.push(n)**, **S.top()**, and **S.pop()** do for integer arguments.

This exemplifies the tradeoffs that come with handling arrays and other collections. If the **while**-loop is executed only a few times, then it is much faster to do elementwise saving of the few elements of the array that are overwritten and restore them one at a time in case of rollback than to save and restore the entire 100,000-element array. But if the loop is executed many times, and many elements of the array are overwritten, then it is faster to simply save the entire array as an atomic data structure and restore the same way on rollback.

But we may not be able to determine statically which of those is the case, which leaves the reverse code generator in a quandary. Which kind of reverse code should it generate? One approach is to allow the programmer to offer advice in the form of a pragma or specially formatted comment indicating which choice to use. Another approach is illustrated on the next slide.



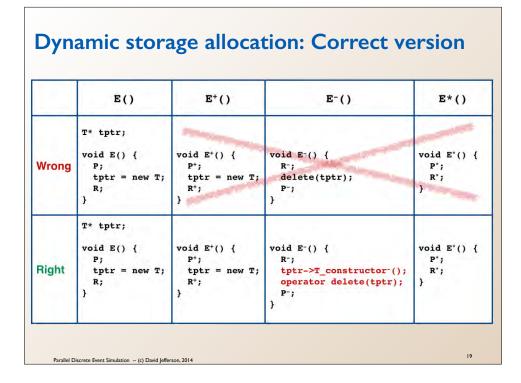
Multiple corrections on this slide thanks to Markus Schordan.

In this example the **A** and **B** arrays are state variables, but **i** is not. The functions **th**, **test**, **f**, and **g** are all side-effect free. The function **S.pushArray(A)** is intended to push the entire array **A** onto the stack, even though this is not strictly correct C++; likewise **S.topArray(A)** is intended to copy the entire array value from the top of the stack into **A**. **S.popArray()** just pops off the entire array.

Here we do something sophisticated. We do not decide decide statically whether to use elementwise or atomic saving of array **A**. *Instead, we make some measurements at runtime and decide then.* Whether this method will prove practical or not (i.e. whether we can automatically generate code like this) is an open research question, but this example illustrates the depth and complexity of the options we have in generating good forward and reverse code.

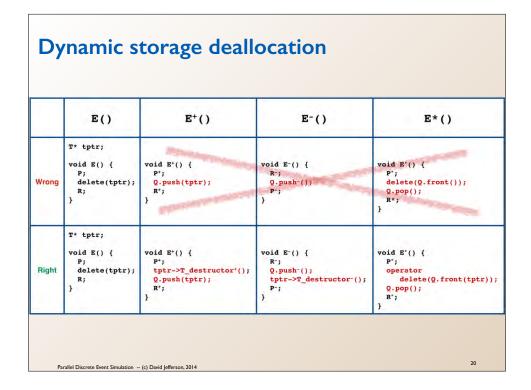
In the forward method $\mathbf{E}^+(\mathbf{)}$ we don't know how many times the loop will be executed, and so we start out assuming that we will be doing incremental saving of array elements as they are modified, keeping count of the number of loop executions and also pushing array elements onto stack \mathbf{s} as they are modified. But after a certain threshold number of elements have been pushed onto the stack, it becomes probable the loop will cycle many times and that a large fraction of all of the array elements will be modified. At that point we abandon elementwise saving and pop everything off of the stack that we pushed onto it. We then start over, pushing the entire array \mathbf{A} onto the stack all at once. If, at the end of the loop, we did not abandon elementwise saving of array \mathbf{A} , then we push the loop count \mathbf{ct}

on the stack to prepare for elementwise restoration. Either way, the last thing we push on the stack is the boolean **elementwise** to tell the reverse method **E**() which mechanism to use to restore **A**.



Calling **new T** in the forward method is OK. But we must realize that this invokes a constructor for type **T** in C++, (which in turn invokes a whole hierarchy of other constructors for members in tyre and parent types, all called in canonical order). The problem with treating **delete()** as its reverse is that **delete()** calls the *destructor* for the type of data being deleted, *but the destructor is not generally the reverse of the constructor*! In general, programmer-defined constructors can have arbitrary side-effects on other variables, which may or may not be exactly reversed by the matching destructor. What we need is for each constructor to have a

reverse constructor for T, which we denote here by **T_constructor**(). We call that first, and the use the obscure C++ construct **operator delete()** to finally free the storage allocated by **new**. In C++ **operator delete()** does not call a destructor.



As with new, a similar problem arises in reversing **delete()** in that **delete()** invokes the destructor for the type of object being destroyed. We need to invoke that destructor in the forward routine, because the side effects of the destructor on other variables can be felt in subsequent statements (**R**). However, we need to run not the original destructor, **tptr->T_destruct()**, but its forward instrumented version, **tptr->T_destruct+()**. And we need to be able to reverse the effects of **tptr->T_destruct+()** in the **E**() by calling **tptr->T_destruct-()**. But while we have to reverse the effects of the destructor in **E**(), we cannot really free the storage associated with the object in the **E**() **routine**, because if we do the storage could be re-allocated and overwritten, and the overwriting would make it impossible for us to reverse the action in **E**(). So we delay the actual freeing of the storage until the commit routine, and then we do it using the **operator delete()** construct.

E()	E+()	E-()	E*()
<pre>File file; void E() { P; file.output(expr); R; }</pre>	<pre>void E*() { P*; Q.push(file); value = expr; Q.push(value); R*; }</pre>	<pre>void E⁻() { R⁻; Q.push⁻(); Q.push⁻(); P⁻; }</pre>	<pre>void E*() { P'; File f = Q.front(); Q.pop(); f.output(Q.front()); Q.pop(); R'; }</pre>
• Both file a	nd the value of ex	pr must be pus	shed into the
stack by val	ue		
stack by val	uates to an array, t	he entire array	must be pushed
stack by values of the stack by value on the state on the state of the	uates to an array, t ck uates to a struct, c using an appropr	or class type, fu	Ily deep copies

Note: **expr** is a side-effect free expression. When we push data onto the queue **Q** for processing at commit time, we must push *values*, not expressions to be evaluated.

A reasonable first strategy for generating reverse code

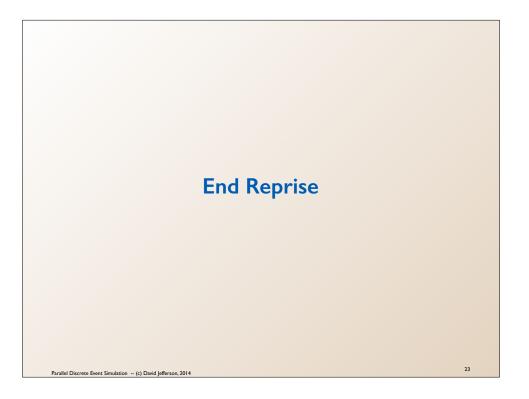
- · Consider each variable or field v in the object's state separately
- If v is not modified by E(), do nothing to save or restore it
- If v's initial value can be regenerated from the final state produced by E(),

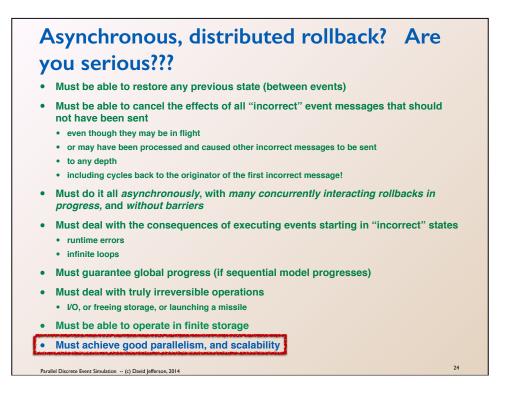
generate code in $E^-()$ to do that using path-oriented, regenerative methods (yielding "perfect reversibility" for that variable).

- If v is a scalar (including pointers code)
 - if modified many times in E(), do up front state saving and restoration
 - if modified only once in E(), do incremental state saving and restoration
 - if cannot be sure statically, try to determine the places where it is modified the *first* time, and put saving and restoration code at those places
 - · otherwise do incremental state saving
- · If v is an array or collection
 - if most of the elements are modified in E(), do up front state saving and restoration of the entire collection
 - if only a few of the elements are changed, do incremental state saving on an element-by-element basis
 - if it cannot be statically determined which, consider programmer pragmas, or dynamic instrumentation to measure the number of elements affected
- If v is a class variable
 - Establish proper constructor, copy constructor, destructor and operator = functions for the class
 - * Generate reverses for those functions and generate appropriate code in E⁺(), E⁻(), and E^{*}() using them

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At this point in the course we have demonstrated the Optimistic (rollback-based_ methods have all of the good properties shown in green. But we have not yet demonstrated that good performance can be achieved at large scale. We will be doing that in the next few dozen slides.

Ancient data from Army benchmarks running on the Time Warp Operating System (TWOS) on the Caltech Mark III Hypercube

Specs: 64 nodes Motorola 68020 processors, 12 - 30 MHz 6-D hypercube interconnect 4 MB RAM per node

Date: ~1990

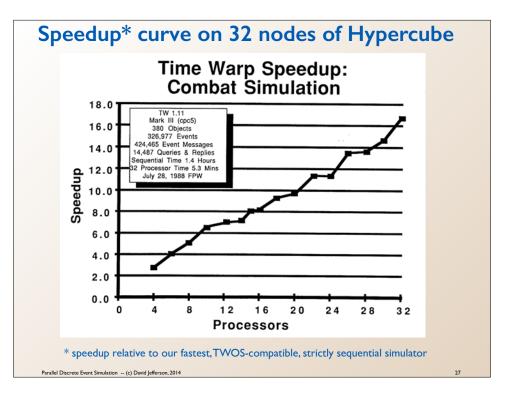
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Features of TWOS in 1990

- 128-bit virtual time; 20-char process name space
- No use of shared memory (even on shared memory Butterfly machine)
- No use of process blocking
- Multi-message events
- Lazy & aggressive cancellation
- Full cancelback mechanism for storage mgt and flow control
- One-pass GVT calculation
- Dynamic process creation/destruction
- Dynamic "phases"; phases separately schedulable & migratable; parallelism possible between phases of one process
- Runtime process (phase) migration
- Static & also fully dynamic load balancing
- Highly optimized and perfectly compatible sequential simulator
- Elaborate, automated instrumentation

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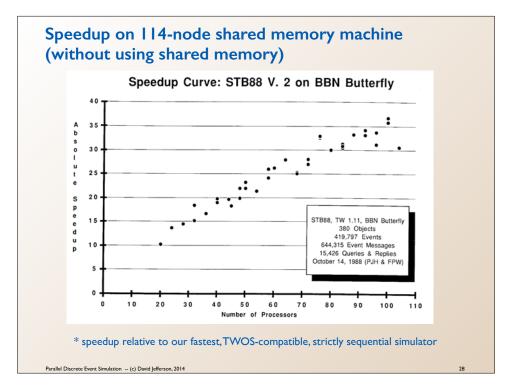
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Combat simulation -- mini JCATS JPL Mark III Hypercube, 32 processors, Motorola 68020, 4 MB RAM per node July 1988

This is a strong scaling speedup curve, and the speedup is compared to an optimized sequential simulator, not against a one-node execution of Time Warp.

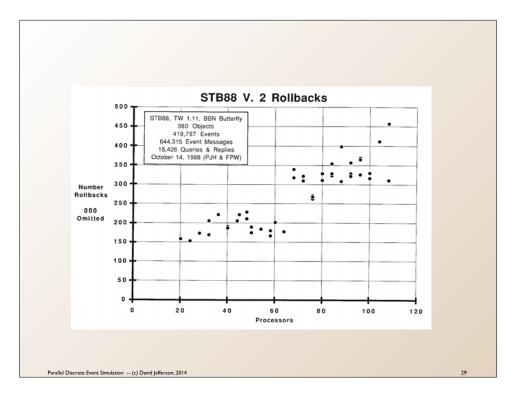
Linear speedup up to 32 nodes, almost 17x speedup



A few months later in October. Same combat model (modified since July), same size, run for a little more time. BBN Butterfly (shared memory, NUMA), 108 processors

Linear speedup to about 80 nodes, then probably some flattening of performance.

Note performance variations in multiple runs of exact same configuration.



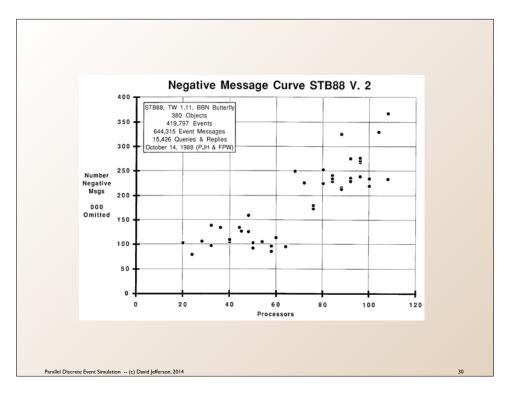
Same runs as on the previous slide with number of rollbacks plotted as function of number of processors.

Note that as more processors are used (strong scaling) the number of rollbacks trends upward!

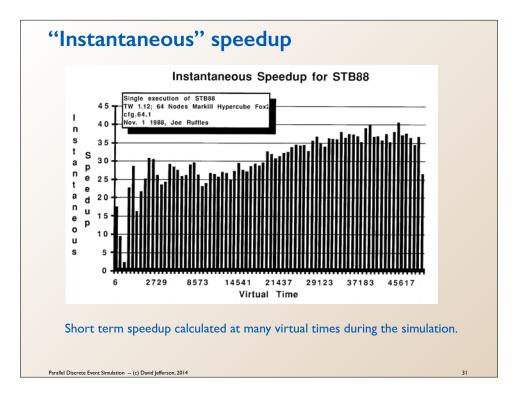
But the previous graph showed that we we still getting good scaling results in spite of that!

This shows that it is not true that we should have as a tuning goal of TW the minimization of the number of rollbacks.

Most these rollbacks (hundreds of thousands of them) occur off the critical path, so they do not slow down the progress of the simulation.



Again, same runs, this time with number of antimessages transmitted plotted as a function of number of nodes. Again, number of antimessages increases, even as speed increases (see two slides previous). Note that antimessages is approximately proportional to the number of rollbacks from the previous slide.



Early experiments with dynamic load balancing

• Migration mechanism:

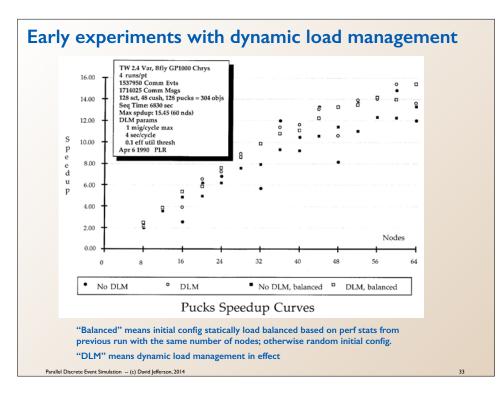
- Split an object P at time τ into two "phases" P[-∞ .. τ) and P[τ .. ∞]
- Leave $P[-\infty .. \tau)$ in place, and migrate $P[\tau .. \infty]$ to new node
- Every node had a full map of object/phase locations. Phase map indicated locations of all *phases* of all objects.
- Phase location map had to be updated globally every migration cycle .
- Rollback could take place across more than one processor
- On the other hand, parallelism in time was possible!

Load balancing policy

- Migrations considered every 4 seconds of execution
- At most one object (phase) migrated on or off a node each migration cycle.
- Low GVT nodes considered overload; high GVT nodes considered underloaded.
- Migrations from most loaded to least loaded node, 2nd most loaded to 2nd least load, etc.

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Different model: "Pucks", a bunch of frictionless hockey pucks colliding with the sides of the rink and with each other

Dynamic load management based on "effective utilization" concept

No DLM:	Random initial arrangement of Objects on nodes; no dynamic load management
DLM:	Random initial arrangement of Objects on nodes, and with dynamic load management
No DLM, balance	d: Statically balanced initial arrangement of nodes, but no DLM

DLM, balanced: Statically balanced initial arrangement of nodes, with DLM in addition

Forward 25 years and a factor of 1 million in scale:

Experiments at extreme scale on Sequoia

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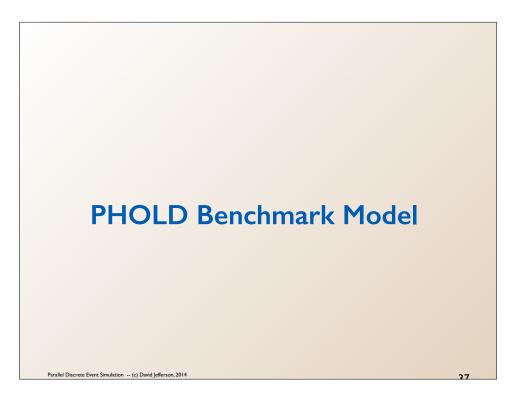


LLNL's Sequoia Blue Gene/Q

- Sequoia: 96 racks of IBM Blue Gene/Q
 - 1,572,864 A2 cores @ 1.6 GHz
 - 1.6 PiB of RAM
 - 16.32 Pflops for LINPACK/Top500
 - 20.1 Pflops peak
 - 5-D Torus: 16x16x16x12x2
 - Bisection bandwidth ~49 TB/sec
 - Power ~7.9 MWatts

- "Super Sequoia" @ 120 racks
 - 24 racks from *Vulcan* added to the original 96 racks
 - Increased to 1,966,080 A2 cores
 - 5-D Torus: 20x16x16x12x2
 - Bisection bandwidth same
 - Other performance metrics increased proportionally

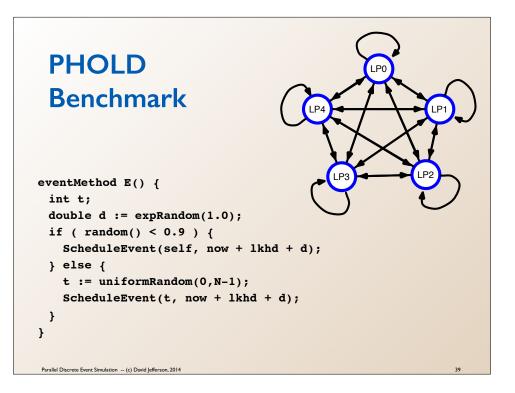




PHOLD Benchmark model

- Defined by Richard Fujimoto
- De facto standard for comparing parallel discrete event simulators to one another
 - In use for >20 years
- Artificial model representing challenging simulation dynamics
 - Very fine-grained events
 - No communication locality
 - Symmetric and (statistically) load balanced, steady state equilibrium
 - Almost all simulator overhead
- We did an exact strong scaling study:
 - Same number of LPs, same random seeds, same events, and same exact results at all scales
 - Only variable is the number of nodes (racks) of Sequoia used

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 \mathbf{N} = number of LPs in the simulation

self = the name of the LP executing

uniformRandom(m,n) draws an integer in the inclusive range [m..n]

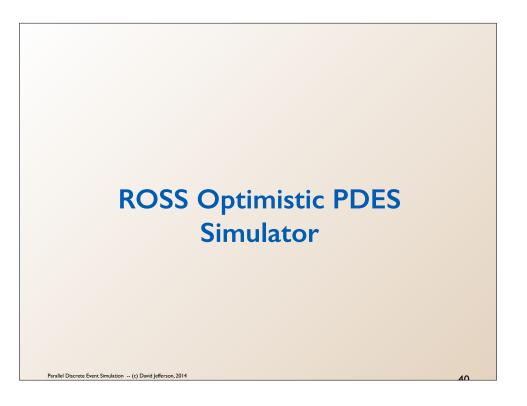
 $expRandom(\lambda)$ draws a random real number exponentially distributed with mean λ

random() draws a real number uniformly distributed in the range [0.0..1.0)

now is the current simulation time in the calling object

1khd is the lookahead value.

1khd can be 0 for optimistic simulations, but is generally made nonzero so that comparisons can be made with conservative algorithms (which need nonzero lookahead). It was 0.1 throughout this study.

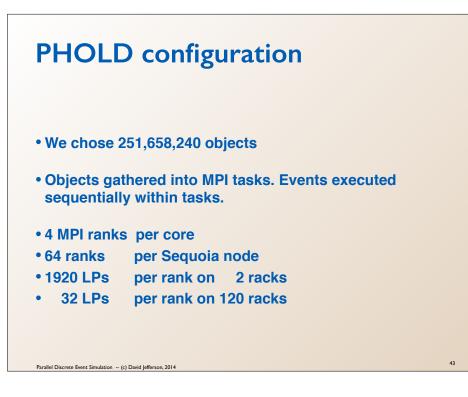


ROSS parallel discrete event simulator

- <u>Rensselaer Optimistic Simulation System</u>
- Developed at RPI by Prof. Chris Carothers
- Open source
- Uses Time Warp optimistic synchronization
 - rollback + antimessages
- Supports reverse computation
 - In this case the reverse methods were produced by hand, not using Backstroke
 - Used a perfectly reversible random number generator

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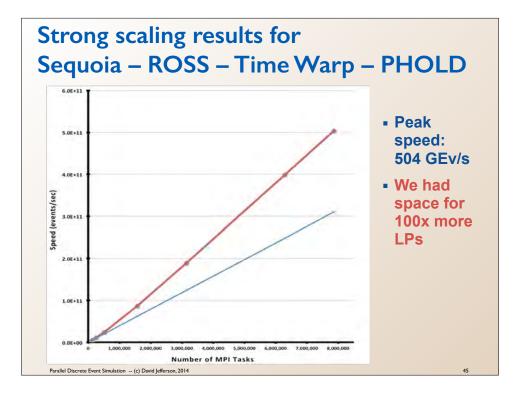


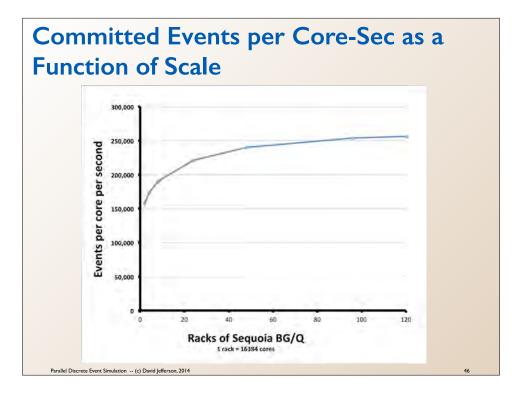


Difficulties encountered at all software levels

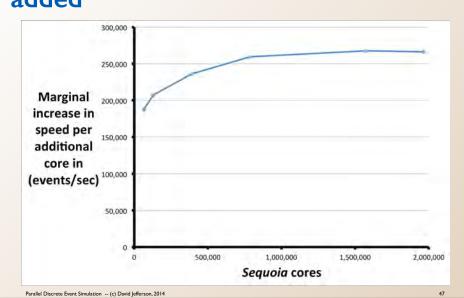
- Subtle "bug" in PHOLD benchmark
 - Only apparent when looking at the results in the 9th decimal place
- Scaling bugs in ROSS
 - Had to re-do internal memory management completely
- Scaling bugs or limitations in MPI
 - Did not work at full scale with 4 LPs per core
 - Memory/buffer management issue in MPI_Init()

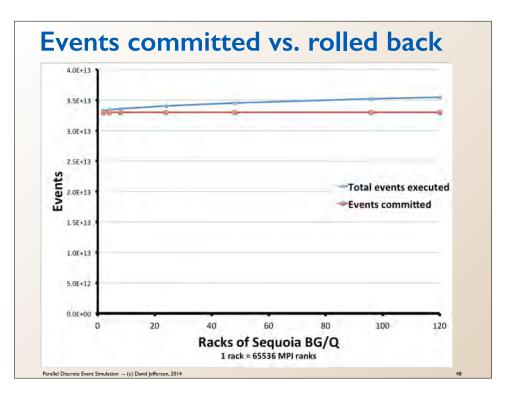
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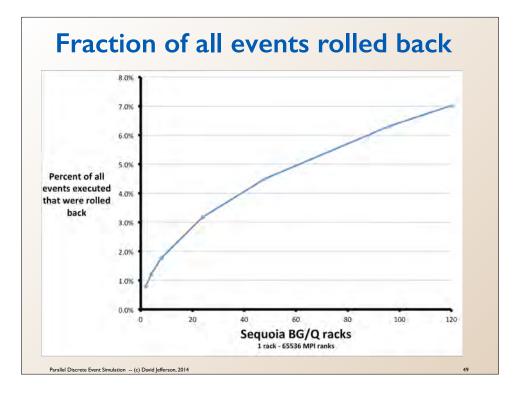
Diminishing returns as more cores added



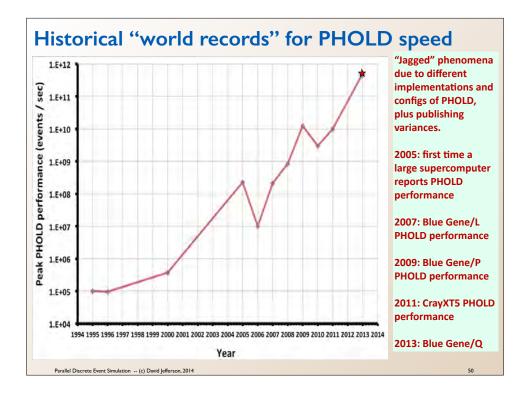


The difference between the two curves is the number of events rolled back.

Events committed is exactly constant because this is a strong scaling study, and so we are committing exactly the same events at all scales.

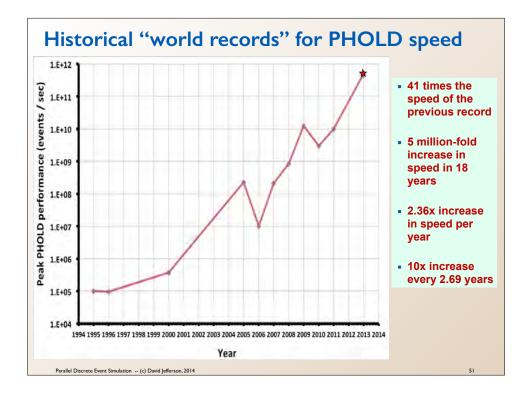


Of all of the events executed (committed + rolled back) this is the fraction rolled back, i.e. rolledback/(rolledback+committed).



This logarithmic plot is the historical record of PHOLD benchmark speeds published. Each point plots the highest speed published on this benchmark in the year, regardless of platform. In most cases the authors just configured the fastest PHOLD instance they could on the biggest machine they had access to and reported the (among much other detail) the resulting speed in events per second.

The "jagged" look of the graph has a mixture of causes: The implementations, parameters, and configurations of PHOLD were not identical from year to year. And some years no one beat the previous records, but since they (quite appropriately) published their results anyway, we plotted them.

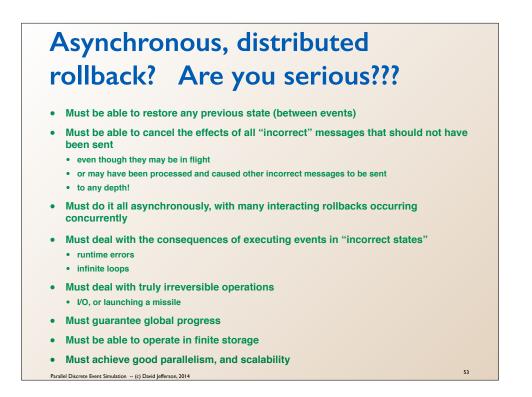


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		BG/Q nodes	BG/Q cores	MPI ranks	PHOLD LPs	LPs per Rank	Events Processed		Events Rolled Bac	Net Events	Net Events Processed	Efficiency (my def)	Running Time (sec)	Event Rate (ev/sec)		Event rate per core ev/(core*sec)
											(string form)					
8-Mar-13	2	2048	32768	131072	251658240	1920	3.325E+13	1.66264E+13	2.679E+11	3.298E+13	"32984968283642"	0.99194	6377.8	5.17E+09	0.71	157832.5
1-Mar-13	4	4096	65536	262144	251658240	960	3.339E+13	8.34757E+12	4.053E+11	3.298E+13	"32984968283642"	0.98786	2911.6	1.13E+10	1.05	172865.
8-Mar-13	8	8192	131072	524288	251658240	480	3.358E+13	4.19795E+12	5.986E+11	3.298E+13	"32984968283642"	0.98218	1323.8	2.49E+10	1.40	190101
8-Mar-13	24	24576	393216	1572864	251658240	160	3.407E+13	1.41953E+12	1.084E+12	3.298E+13	"32984968283642"	0.96819	379.7	8.69E+10	1.94	220896
B-Mar-13	48			3145728	251658240			7.19565E+11		3.298E+13	"32984968283642"	0.95500	174.7	1.89E+11	2.28	240135.
8-Mar-13 1-Mar-13	96	98304			251658240 251658240			3.66734E+11 2.95624E+11			"32984968283642" "32984968283642"	0.93690	82.6		2.60	253854. 256360.

Part of the original data collect for our Sequoia / ROSS/ Time Warp / PHOLD study.



This shows the list of challenges we have to overcome for the Time Warp algorithm, or any optimistic PDES algorithm, to be practical.

I have now completed the list.

Concluding Thoughts

- This record will stand until at least the next generation of supercomputers
 - Parallel DES needs cores, not GPUs
 - No existing machine today can challenge Sequoia in that regard
 - except Tianhe-2 in Guangzhou, which has 3.12 million Xeon cores
- We had enough RAM at full scale for 100x the number of LPs we actually used
 - i.e. 25 billion LPs (or "objects", "entities", "agents", etc.)
- · We propose a logarithmic measure of speed on this benchmark
 - Warp = log₁₀(Speed) 9
 - By this measure we achieved Warp 2.70
- This result opens the door to "planetary scale" simulations
 - "only" 7 billion people on Earth
 - "only" 4 billion IPv4 addresses
 - "only" 50 billion cells mouse
 - "only" 1 trillion atoms in a cubic micron of solid metal

We are now more limited by our ability to build and validate discrete models than by our ability to execute them!

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Thank You!

ACKNOWLEDGEMENTS: This work was performed under the auspices of the U.S. Department of Energy by Lawrence Livermore National Laboratory under contract DE-AC52-07NA27344, and Argonne National Laboratory (``Argonne'') under Contract No. DE-AC02-06CH11357. This research used the *Sequoia* IBM Blue Gene/Q computing resources at Lawrence Livermore National Laboratory, and additional Blue Gene/Q computing resources were provided by the Computational Center for Nanotechnology Innovations (CCNI) located at Rensselaer Polytechnic Institute. Part of the CCNI's Blue Gene/Q was acquired using funds from the NSF MRI Program, Award #1126125. Prof. Carothers' time was supported by AFRL Award FA8750-11-2-0065 and DOE Award DE-SC0004875.

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A word from our sponsors.

Next Two Weeks!

- Two "finale" lectures
- · I will make an audacious but speculative argument
 - Optimistic parallel discrete event simulation can be viewed as a new parallel programming paradigm for many scalable applications, not just simulation.
 - Put another way: From this point of view all sufficiently large cooperative parallel computations can fruitfully be viewed as simulations
- Will touch on many exascale (and beyond) issues
 - synchronization
 - debugging
 - fault recovery
 - load balancing
 - power management
 - space-time symmetry
 - parallel programming methodology
- Looking for your feedback and ideas!

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